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PATENT

Case Docket No.: P9119 US

Date: December 28, 2000

jc984 U.S. PTO  
12/28/00

jc945 U.S. PTO  
09/751551  
12/28/00

ASSISTANT COMMISSIONER FOR PATENTS  
Washington, D.C. 20231  
Box PATENT APPLICATION

Sir:

Transmitted herewith for filing is the patent application of:

Inventor: Lian, et al.

For: MULTI-LAYER Pt ELECTRODE FOR DRAM AND  
FRAM WITH HIGH K DIELECTRIC MATERIALS

This application includes:

16 pages: specification, claims and abstract  
6 sheets of drawings,    formal/X informal  
   photographs

Also enclosed is:

   Declaration and Power of Attorney  
   Information Disclosure Statement pursuant to 37 CFR 1.56.

The filing fee has been calculated as shown below:

FOR:	(Col. 1) NO. FILED	(Col. 2) NO. EXTRA RATE	OTHER THAN A SMALL ENTITY FEE
BASIC FEE	XXXXXXX	XXXXXXXXXX	XXXXX \$710
TOTAL CLAIMS	20 =	0	x 18= \$0
INDEP CLAIMS	3 =	0	x 80= \$0
* MULTIPLE DEPENDENT CLAIM PRESENTED			+260.= \$0
			TOTAL \$710

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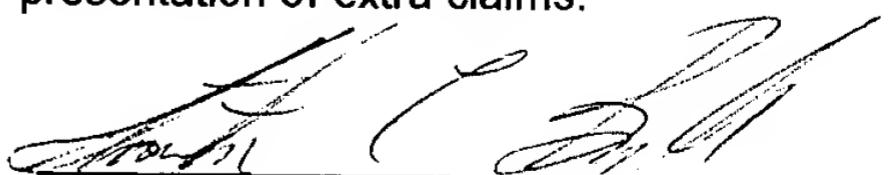
Any additional filing fees required under 37 CFR 1.16.

Any patent application processing fees under 37 CFR 1.17.

The Commissioner is hereby authorized to charge payment of the following fees during the pendency of this application or credit any overpayment to Deposit Account No. 19-2179. A duplicate copy of this sheet is enclosed.

Any patent application processing fees under 37 CFR 1.17.

Any filing fees under 37 CFR 1.16 for presentation of extra claims.



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